HIERARCHICAL MULTI-COMPONENT TRACE FACILITY USING MULTIPLE BUFFERS PER COMPONENT

ABSTRACT OF THE INVENTION

Disclosed is a system and a method for implementing trace buffers in a computer program. A trace facility monitors operation of the computer program in real time. As the code is executed, error-related events occur and are divided into at least three components, including errors, warnings, and messages. Records of these events are stored in the trace buffers. Each task, process, or component of the embedded system that is traced may have an independent set of trace buffers. Unique events eliminate the confusion as to where or when an event occurs in a system. Separate trace buffers for each component of the embedded system ensure that no component consumes buffer resources needed by other components. The trace facility may be embedded in the code in a transparent manner and may be utilized to locate errors on-site.

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